MOTOROLA

DL156/D
REV 4

Fast Static RAM
Component and Module Data

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Fast Static RAM
Component and Module Data

Motorola offers a broad range of fast SRAMs for virtually any digital data processing system application. This data book contains complete specifications for individual FSRAM circuits in data sheet form, as well as an explanation of Motorola's reliability and quality program and an applications section.

For information on Dynamic RAM devices, please refer to DL155/D.

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MCM69L818A	256K x 18	7.5/8.5/9.5/10.5	Late Write HSTL, 3.3 V	3–154
MCM69L819	256K x 18	8/9	Late Write LVTTL, 3.3 V	3–173
MCM69L819A	256K x 18	7.5/8.5/9.5/10.5	Late Write LVTTL, 3.3 V	3–191
MCM69L820	256K x 18	8/9	Late Write 2.5 V I/O, 3.3 V	3–209
MCM69L820A	256K x 18	7.5/8.5/9.5/10.5	Late Write 2.5 V I/O, 3.3 V	3–227
MCM69P536A	32K x 36	5/6/7	Pipelined BurstRAM, 3.3 V	4–175
MCM69P536B	32K x 36	4/4.5/5/6/7	Pipelined BurstRAM, 3.3 V	4–186
MCM69P536c	32K x 36	4/4.5/5/6/7	Pipelined BurstRAM, 3.3 V	4–197
MCM69P618A	64K x 18	4/4.5/5/6/7	Pipelined BurstRAM, 3.3 V	4–102
MCM69P618C	64K x 18	4/4.5/5/6/7	Pipelined BurstRAM, 3.3 V	4–113
MCM69P737	128K x 36	3.5/3.8/4	Flow–Through BurstRAM, 3.3 V	4–51
MCM69P819	256K x 18	3.5/3.8/4	Pipelined BurstRAM, 3.3 V	4–19
MCM69Q536	32K x 36	6/8/10	Single address, separate I/O SRAM	5–167
MCM69Q618	64K x 18	6/8/10	Single address, separate I/O SRAM	5–190
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MCM69R537	32K x 36	5/6/7/8	Late Write LVTTL, 3.3 V	3–264
MCM69R538	32K x 36	56/7/8	Late Write 2.5 V I/O, 3.3 V	3–283
MCM69R539	32K x 36	4.5/5	Late Write HSTL, dual clock, 3.3 V	3–302
MCM69R618	64K x 18	5/6/7/8	Late Write HSTL, 3.3 V	3–245
MCM69R619	64K x 18	5/6/7/8	Late Write LVTTL, 3.3 V	3–264
MCM69R620	64K x 18	5/6/7/8	Late Write 2.5 V I/O, 3.3 V	3–283
MCM69R621	64K x 18	4.5/5	Late Write 2.5 V I/O, dual clock, 3.3 V	3–302
MCM69R736	128K x 36	7/8	Late Write HSTL, 3.3 V	3–3
MCM69R736A	128K x 36	7/8	Late Write HSTL, 3.3 V	3–22
MCM69R737	128K x 36	7/8	Late Write LVTTL, 3.3 V	3–41
MCM69R737A	128K x 36	5/6/7/8	Late Write LVTTL, 3.3 V	3–59
MCM69R738	128K x 36	7/8	Late Write LVTTL 2.5 V I/O, 3.3 V	3–77
MCM69R738A	128K x 36	5/6/7/8	Late Write LVTTL 2.5 V I/O, 3.3 V	3–95

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MCM69R818A	256K x 18	7/8	Late Write HSTL, 3.3 V	3–22
MCM69R819	256K x 18	7/8	Late Write LVTTL, 3.3 V	3–41
MCM69R819A	256K x 18	5/6/7/8	Late Write LVTTL, 3.3 V	3–59
MCM69R820	256K x 18	7/8	Late Write LVTTL 2.5 V I/O, 3.3 V	3–77
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